IN THE CLAIMS:

1. A method for maintaining cache coherency in a system having a first controller and a second controller, comprising:

selecting a mirror cache line in a second controller to copy data into;

mirroring the data from a cache line in a first controller to the mirror cache line in the second controller; and

sending a message from the first controller to the second controller informing the second controller of cache meta data associated with the mirror cache line.

2. The method of claim 1, wherein the first controller has information about the content of the second controller's cache, and the second controller has information about the content of the first controller's cache.

3. The method of claim 1, wherein the cache meta data includes a logical unit, a logical block, a dirty bit map, and a cache identifier for the data associated with the mirror cache line.

4. The method of claim 1 further comprising placing the address of the mirror cache line into the first controller's hash table.

5. The method of claim 1 further comprising placing the address of the mirror cache line into the first controller's write back queue.

6. The method of claim 1 further comprising managing a free list of mirror locations in the second controller's cache by the first controller, and managing a free list of mirror locations in the first controller's cache by the second controller, wherein the first and second controllers can copy data into the second and first controller's mirror locations, respectively.

7. The method of claim 1, further comprising sending a message from the first controller to the second controller, requesting ownership of a cache line owned by the second controller.

8. The method of claim 7, further comprising granting ownership of a cache line owned by the second controller, to the first controller, wherein, subsequent to the first controller receiving data that is to be mirrored to the second controller cache line, transferring data to the second controller's cache line from the first controller.

9. The method of claim 8, further comprising sending a message from the first controller to the second controller providing the second controller of cache meta data associated with the mirror cache line.

10. The method of claim 1, further comprising switching ownership of cache lines between the first controller and the second controller.

11. The method of claim 1, further comprising, during failback, transferring cache lines from a survivor controller that owns a substantial number of mirror cache lines, to a replacement controller unit.

12. The method of claim 1, further comprising, sending a message to a controller of a mirror cache line, informing the controller that the associated cache line will be flushed and the data associated with the mirror cache line will be temporarily invalid.

13. The method of claim 12, wherein sending the message to the controller of the mirror cache line further comprises, informing the controller that after the associated cache line is destaged, and the mirror cache line's data is consistent with a backing disk and need not be written to the backing disk in the event of a failover.

14. The method of claim 1, further comprising, indicating to the first and second controller that a cache line that has been flushed of data, is available for reuse.

15. A controller system for maintaining cache coherency, comprising:

a disk array,

a first controller, coupled to the disk array, for selecting a mirror cache line on a second controller; and

an interface for mirroring the data from a first controller cache line to the second controller cache line;

wherein a message is sent from the first controller to the second controller informing the second controller about cache meta data associated with the mirror cache line.

16. The controller system of claim 15 wherein the first controller has information about the content of the second controller's cache, and the second controller has information about the content of the first controller's cache.

17. The controller system of claim 15, wherein the cache meta data includes a logical unit, a logical block, a dirty bit map, and a cache identifier associated with the mirror cache line.

18. The controller system of claim 15 further comprising the first controller placing the address of the mirror cache line into the first controller's hash table.

19. The controller system of claim 15 further comprising the first controller placing the address of the mirror cache line into the first controller's write back queue.

20. The controller system of claim 15 further comprising the first controller managing a free list of mirror locations in the second controller's cache, and the second controller managing a free list of mirror locations in the first controller's cache, wherein the first and the second controller can copy data into the second and the first controller mirror locations, respectively.

21. The controller system of claim 15, further comprising the first controller sending a message to the second controller, requesting ownership of a cache line owned by the second controller.

22. The controller system of claim 21, wherein the second controller sends a message granting ownership of a cache line owned by the second controller, to the first controller, wherein, subsequent to the first controller receiving data that is to be mirrored to the second controller cache line, transferring that data to the second controller's cache line from the first controller.

23. The controller system of claim 22, further comprising the first controller sending a message from to the second controller informing the second controller of cache meta data associated with the mirror cache line.

24. The controller system of claim 15, wherein the first and second controllers switch ownership of cache lines.

25. The controller system of claim 15, further comprising, during failback, a survivor controller that owns a substantial number of mirror cache lines transfers associated cache lines to a replacement controller unit.

26. The controller system of claim 15, wherein a message is sent to a controller of a mirror cache line, informing the controller that the associated cache line will be flushed and the data associated with the mirror cache line will be temporarily invalid.

27. The controller system of claim 26, wherein the message that is sent to the controller of the mirror cache line further comprises, informing the controller that after the associated cache line is destaged, the mirror cache line's data is consistent with a backing disk and need not be written to the backing disk in the event of a failover.

28. The controller system of claim 15, further comprising, indicating to the first and second controller that a cache line that has been flushed of data is available for reuse.

29. An article of manufacture comprising a program storage medium readable by a computer, the medium tangibly embodying one or more programs of instructions executable by the computer to perform a method for maintaining cache coherency, the method comprising:

selecting a mirror cache line in a second controller to copy data into;

mirroring the data from a cache line in a first controller to the mirror cache line in the second controller; and

sending a message from the first controller to the second controller informing the second controller of cache meta data associated with the mirror cache line.

30. A storage system for maintaining cache coherency, comprising:

means for selecting a mirror cache line in a second controller to copy data into;

means for mirroring the data from a cache line in a first controller to the mirror cache line in the second controller; and

means for sending a message from the first controller to the second controller informing the second controller of cache meta data associated with the mirror cache line.